

	Title	Issue Date	Current OR	Document ID	Current XRef	Inventor
1	Scheduling and arbitration arrangement in digital data switching system, has ingress, egress port tables and central allocation table each having specific entries for bandwidth allocation	20000629		WO 200038376 A1		
2	Method and system for switch fabric flow control	20031125	370/229	US 6654343 B1	370/236; 370/389	Brandis, Dirk Kurt et al.
3	Method and apparatus for weighted arbitration scheduling separately at the input ports and the output ports of a switch fabric	20040629	370/230	US 6757246 B2	370/229	Alasti, Mehdi et al.
4	Apparatus and method for processing multicast cells in an input-queued multicast switch	19990713	370/230	US 5923644 A	370/415; 370/429	McKeown, Nicholas William et al.
5	Method for scheduling transmissions in a buffered switch	20020319	370/230	US 6359861 B1	370/397; 370/413	Sui, Kai-Yeung S. et al.
6	Queue size arbitration method and apparatus to enhance performance of crossbar cell switch	20040406	370/392	US 6717945 B1	370/395.71; 370/415	Jue, Reginald et al.
7	Process for optimized transmission of ATM cells over connection elements	20030422	370/395.4	US 6553033 B1	370/535	Wallmeier, Eugen
8	Method and apparatus for resource arbitration	19931130	370/396	US 5267235 A	340/2.24; 340/2.7; 370/416	Thacker, Charles P.
9	Data communications	20020409	370/412	US 6370148 B1	370/462	Calvignac, Jean et al.
10	Method and apparatus for scheduling cells in an input-queued switch	19960319	370/412	US 5500858 A		McKeown, Nicholas W.
11	Method and apparatus for providing work-conserving properties in a non-blocking switch with limited speedup independent of switch size	20030513	370/413	US 6563837 B2	370/415; 370/417	Krishna, Pattabhiraman et al.
12	Mechanism providing concurrent computational/communications in SIMD architecture	19921229	712/22	US 5175858 A		Hammerstrom, Daniel W.